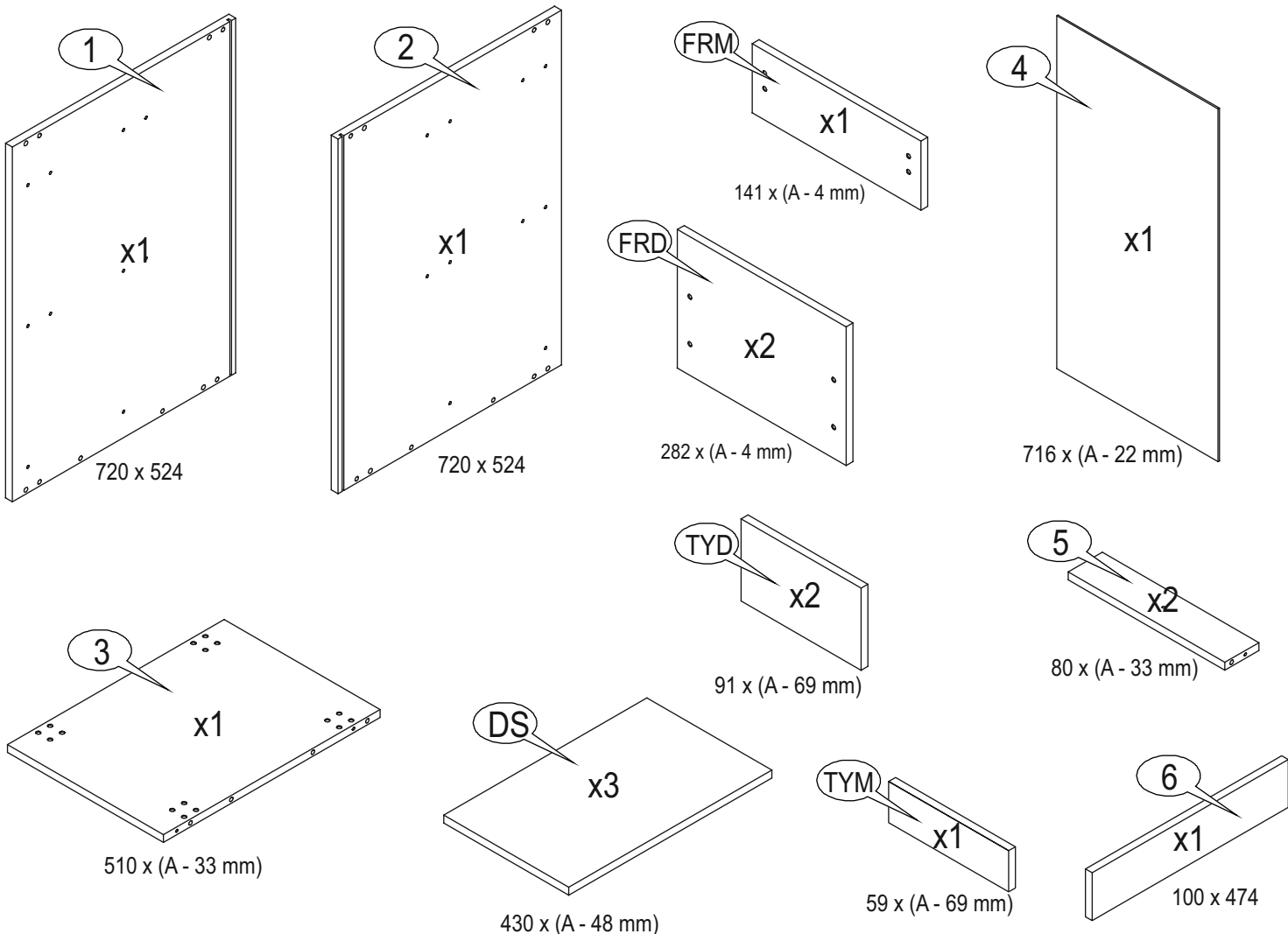
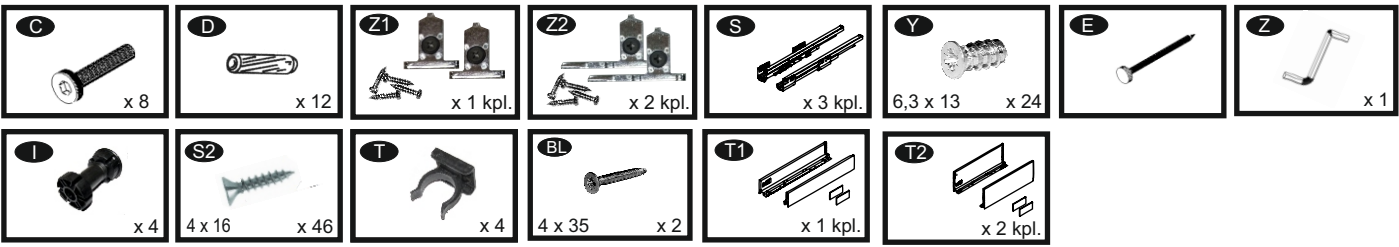
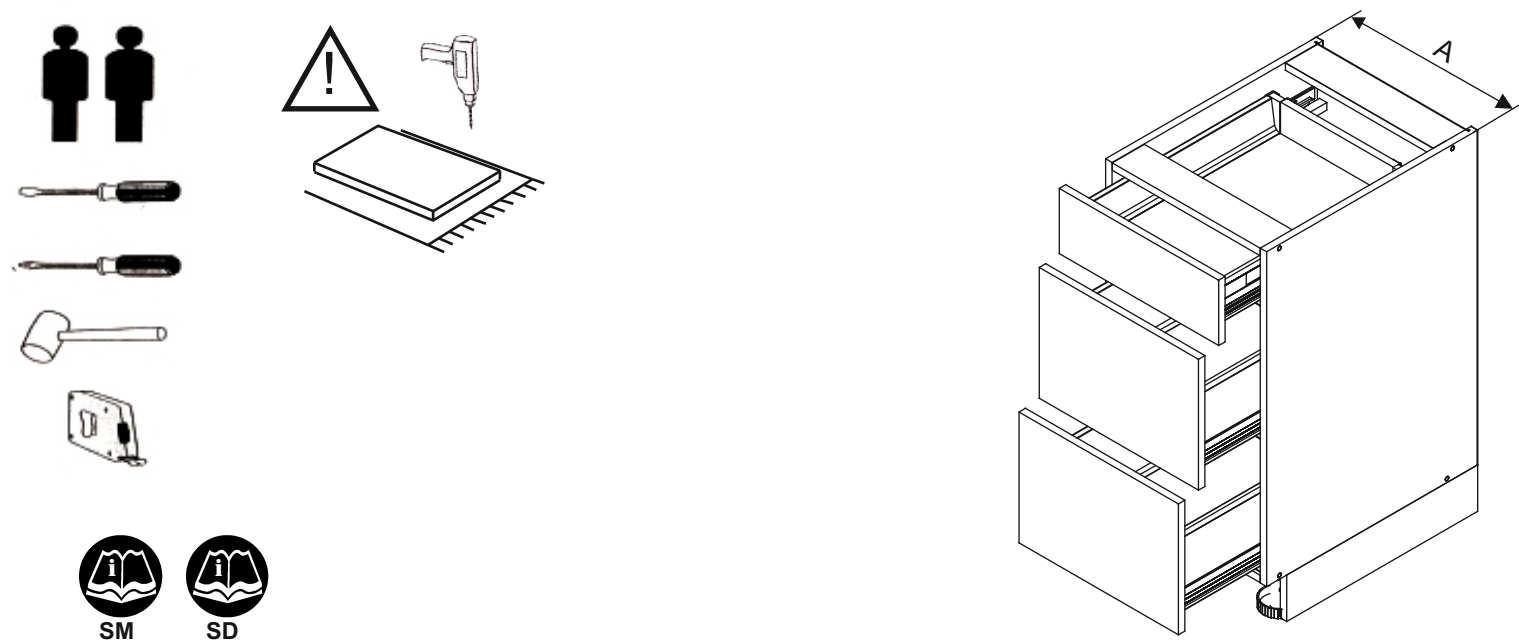
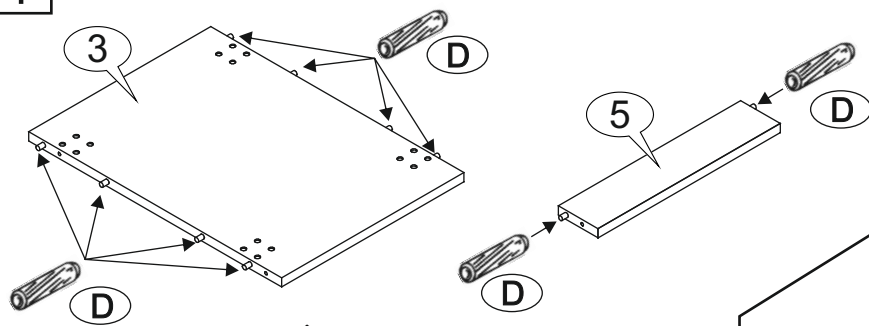
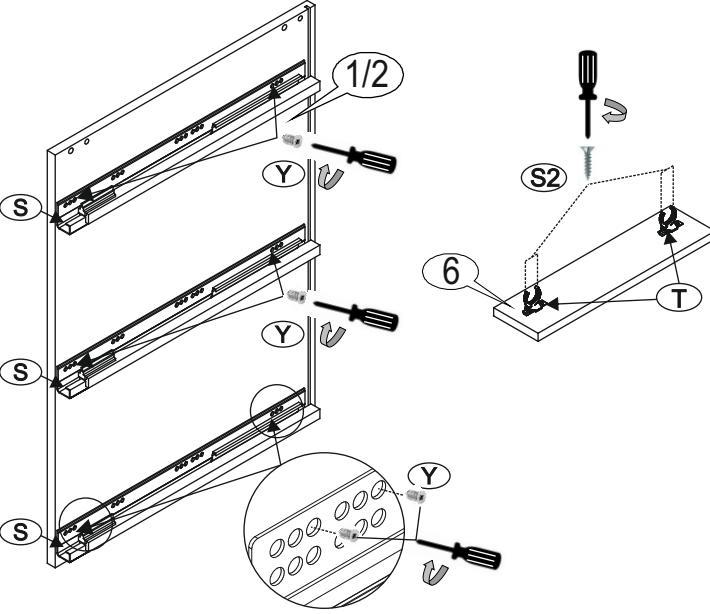




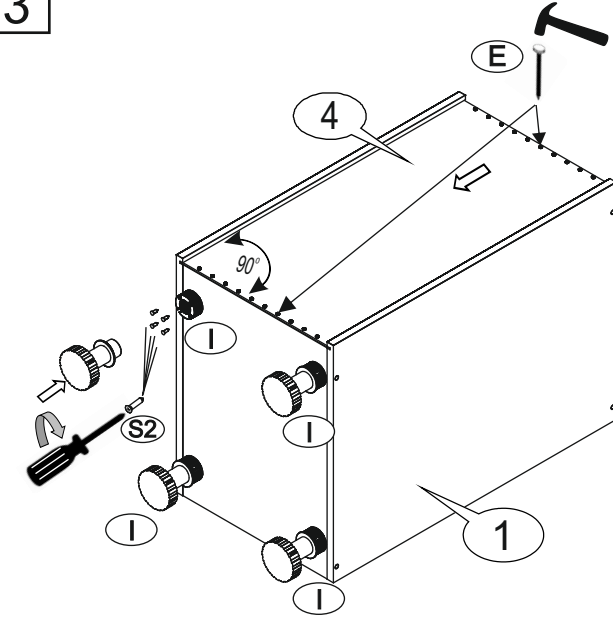
1



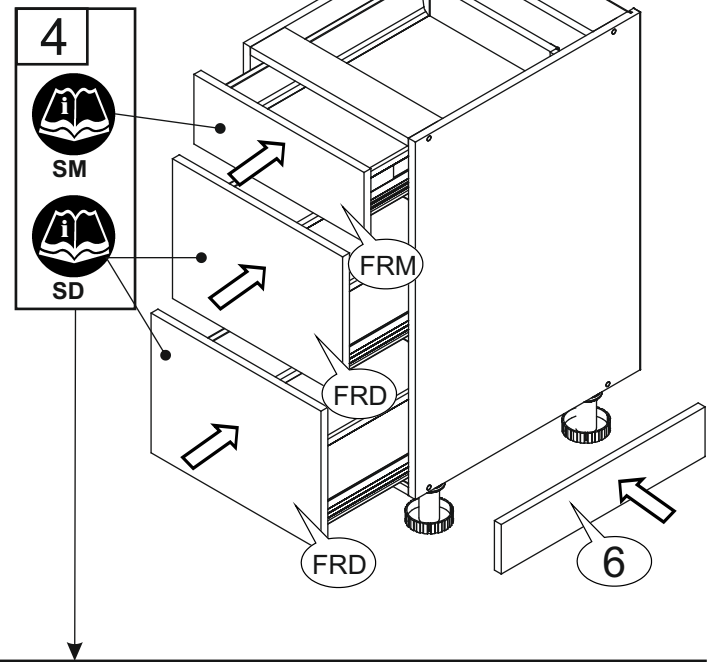
2



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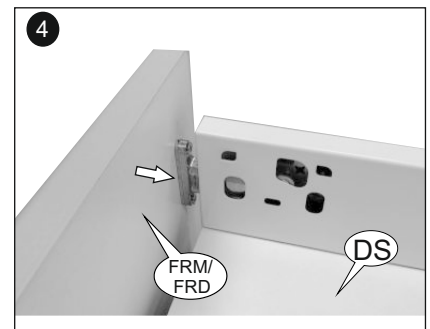
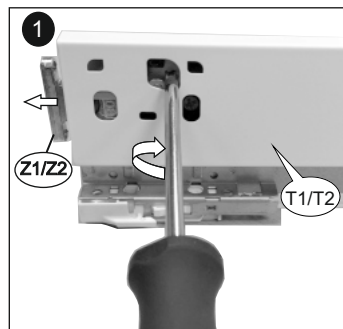
4



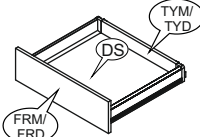
SD



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SM/SD



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